

SEP 06 2005

Attorney Docket No.: YOR920040091US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION of:

Puneet Gupta et al.

Appln. No.: 10/787,488

Art Unit: 2814

Filed: February 26, 2004

Examiner: D. A. WILLE

For: INTEGRATED CIRCUIT LOGIC WITH
SELF COMPENSATING BLOCK
DELAYS**AMENDMENT FAX TRANSMITTAL**

Mail Stop Amendment
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir,

Transmitted herewith are:

An Amendment (6 pages)
 Total number of pages 7 (including this cover sheet and duplicate)
 Please charge any deficiencies in fees and credit any overpayment of fees to attorney's
 deposit account no. 50-0510 and advise us accordingly.

I hereby certify that the above listed documents are being transmitted by fax to the
 Commissioner for Patents, United States Patent and Trademark Office, Washington, D.C. 20231,
 at fax no. (571) 273-8300, this Tuesday, September 06, 2005.

Respectfully submitted,



Charles W. Peterson, Jr.
 Registration No. 34,406

September 6, 2005

(Date)

Customer No. 33233
 Law Office of Charles W. Peterson, Jr.
 11703 Bowman Green Dr,
 Suite 100
 Reston, VA 20171
 Telephone: (703) 481-0532
 Facsimile: (703) 481-0585

RECEIVED
CENTRAL FAX CENTER

SEP 06 2005

Attorney Docket No.: YOR920040091US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION OF:

Puneet Gupta et al.

Appln. No.: 10/787,488

Art Unit: 2814

Filed: February 26, 2004

Examiner: D. A. WILLE

For: INTEGRATED CIRCUIT LOGIC WITH
SELF COMPENSATING BLOCK
DELAYS

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to an Office Action dated June 6, 2005, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this Amendment.

Remarks/Arguments begin on page 6 of this Amendment.